Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("20050094372").PN	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/22 11:06
L2	310	lai-chih-hsi.in. or "quanta computer".as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 11:06
L3	6	("3688635" "4806064" "4855873" "5586005" "6049455" "6201697").PN. OR ("6717815").URPN.	US-PGPUB; USPAT USOCR	OR	ON	2005/09/22 11:19
L4	1281	(361/687).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/22 11:19
L5	5948	display and "first substrate" and process\$4 and "second substrate".clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22:13:01
L6	1	display and "first substrate" and process\$4 and "second substrate" and "heat dissipating device".clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 13:02
L7	4	display and "first substrate" and process\$4 and "second substrate" and "heat dissipating device".	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 13:02
L8	1321	(361/720,709).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/22 13:24

Patent Assignment Abstract of Title

Total Assignments: 1

Application #: 10792332

Filing Dt: 03/03/2004

Patent #: NONE

Issue Dt:

PCT #: NONE

Publication #: <u>US20050094372</u>

Pub Dt: 05/05/2005

Inventor: Chih-Hsi Lai

Title: Electronic device and module structure thereof

Assignment: 1

Reel/Frame: 015047/0956

Received: 03/11/2004

Recorded: 03/03/2004

Mailed: 08/28/2004

Pages: 2

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: LAI, CHIH-HSI

Exec Dt: 02/16/2004

Assignee: QUANTA COMPUTER INC.

NO. 188, WEN HWA 2ND RD.

KUEI SHAN HSIANG

TAO YUAN SHIEN, TAIWAN R.O.C.

Correspondent: THOMAS, KAYDEN & HORSTEMEYER & RISLEY

DANIEL R. MCCLURE

100 GALLERIA, PARKWAY, SUITE 1750

ALTANTA, GA 30339-5948

Search Results as of: 9/22/2005 11:04:02 A.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 571-272-3350 Web interface last modified: Oct. 5, 2002